

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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5-8-02
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In re Application of: : Date: February 7, 2002

Guy E. Averett et al. :

Docket No: ONS00317 :

Filed: Concurrently herewith :

For: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF LOW
SUBSTRATE CAPACITANCE

JCE21 U.S. PTO
10/072145
02/07/02

DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

SIR:

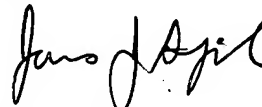
It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted,

Guy E. Averett et al.



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